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**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE REQUESTED
EXAMINING GROUP 2814**

PATENT

Customer No. 22,852

Attorney Docket No. 04329.2613

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Katsuhiko HIEDA)	Group Art Unit: 2814
)	
Application No.: 09/916,509)	Examiner: Le, Thao X.
)	
Filed: July 30, 2001)	
)	
For: FIELD EFFECT TRANSISTOR)	
USING SIDE-WALL GATES TO)	Confirmation No.: 8843
PREVENT PUNCH-THROUGH)	
(As Amended))	

MAIL STOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT AFTER FINAL

In reply to the Office Action mailed February 4, 2005, with a period for response extending through July 5, 2005, (July 4, 2005, being a holiday) by virtue of a Petition for Two Months Extension of Time, please amend the above-identified application as follows:

Amendments to the Drawings are shown in this paper.

Amendments to the Claims are reflected in the listing of claims in this paper.

Remarks begin on page 21 of this paper.

Attachments to this paper include two Replacement Sheets of drawings
including Figs. 79-81.